

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12407-004001	Application No. 09/724,196
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant John Redford	
		Filing Date November 28, 2000	Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
TM	AA	4,604,695	08/1986	Widen et al.			
TM	AB	4,679,140	07/1987	Gotou et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
TM	AQ	John L. Hennessy et al., "Enhancing Vector Performance," COMPUTER ARCHITECTURE A QUANTITATIVE APPROACH, Second Edition, Section B5, pp. B23-B29, 1996.
	AR	
	AS	
	AT	

Examiner Signature <i>Donna Meanske</i>	Date Considered November 17, 2003
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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Patent and Trademark Office

Attorney's Docket No.

12407-004001

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Information Disclosure Statement

ORIGINAL by Applicant

(Use several sheets if necessary)

(37 CFR § 1.98(b))

Applicant

John Redford

Filing Date

November 28, 2000

Group Art Unit

U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
TM	AA	5,555,386	09/1996	Nomura			
TM	AB	5,659,722	08/1997	Blaner et al.			
TM	AC	5,848,290	12/1998	Yoshida et al.			
	AD						
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Foreign Patent Documents or Published Foreign Patent Applications

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	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
TM	AQ	Single-Bit Processor Enable Scheme, IBM Technical Disclosure Bulletin, April 1987, Vol. 29, No. 11, pages 5016-5017.
	AR	
	AS	
	AT	

Examiner Signature

Jona M. Jensen

Date Considered

11/04/03

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